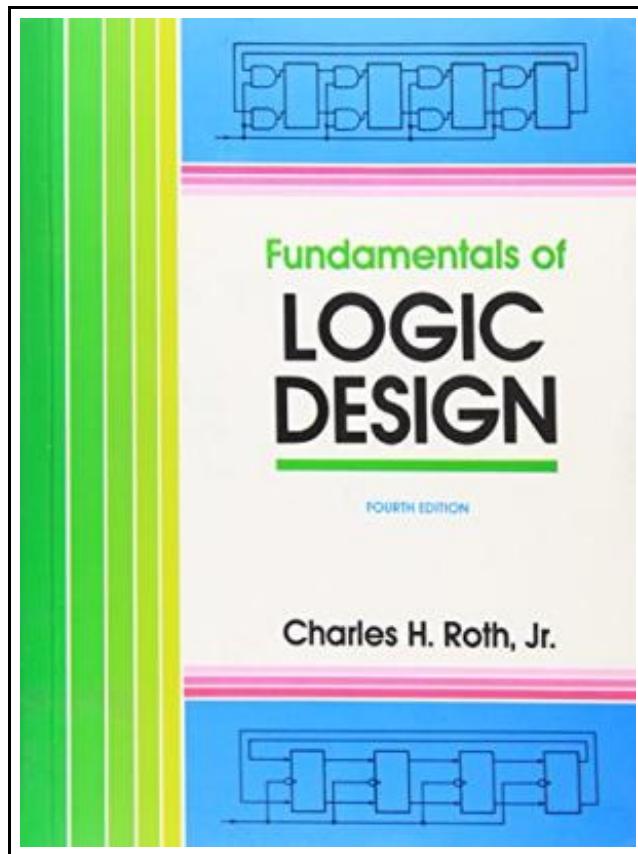


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Jaico Publishing House, 2006. Paperback. Book Condition: New. 4th edition. Although many texts are available in the areas of switching theory and logic design, this text was developed specifically to meet the needs of a self-paced course in which students are expected to study the material on their own. The text is suitable for both computer science and engineering students. All material relating to circuit aspects of logic gates is contained in Appendix A so that this material can conveniently be omitted by computer science students or other students with no background in electronic circuits. The text of the book is designed in such a way that it can be used in either a standard lecture course or in a self paced course. In addition of the standard reading material and problems, study guides and other aids for self study are included in the text. The content of the text is divided into 27 study units. These units form a logical sequence so that mastery of the material in one unit is generally a prerequisite to the study of succeeding units. Each unit consists of four parts. First a list of objectives states precisely what one is expected to learn by studying the unit. Next the study guide contains reading assignments and study questions. The text material and problem set which follow are similar to a conventional text book. The 27 study units are divided into three main groups. The first 10 units treat Boolean algebra and the design of combinational logic networks. Units 11 through 22 are mainly concerned with the analysis and design of clocked sequential logic networks, including networks for arithmetic operations. Units 23 through 27 cover special problems encountered in the analysis and design of asynchronous sequential networks. The text attempts to achieve a balance between...



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